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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/629,903	07/30/2003	Hee Bok Kang	40296-0031	6252	
26633	7590 08/24/2004	,	EXAM	EXAMINER	
HELLER EHRMAN WHITE & MCAULIFFE LLP			DINH,	DINH, SON T	
SUITE 300	1666 K STREET,NW SUITE 300			PAPER NUMBER	
WASHINGT	ON, DC 20006	2824			
			DATE MAILED: 08/24/2004	4	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Summary		10/629,903	KANG ET AL.	KANG ET AL.			
		Examiner	Art Unit	,			
		son t dinh	2824	pro			
Period fo	The MAILING DATE of this communical or Reply	tion appears on the cover sheet	with the correspondence ad	idress			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA nsions of time may be available under the provisions of 3 SIX (6) MONTHS from the mailing date of this communication of the period for reply specified above is less than thirty (30) do period for reply is specified above, the maximum statutoure to reply within the set or extended period for reply will, reply received by the Office later than three months aftered patent term adjustment. See 37 CFR 1.704(b).	TION. 7 CFR 1.136(a). In no event, however, may ation. 14 ays, a reply within the statutory minimum of ry period will apply and will expire SIX (6) M by statute, cause the application to become	y a reply be timely filed thirty (30) days will be considered timel MONTHS from the mailing date of this c a ABANDONED (35 U.S.C. § 133).				
Status							
1)	Responsive to communication(s) filed of	on					
2a)□		$\boxtimes$ This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the m							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)⊠	<ul> <li>✓ Claim(s) <u>1-23</u> is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> </ul>						
5)⊠	Claim(s) 1-16,21 and 23 is/are allowed.						
	Claim(s) 17,19 and 22 is/are rejected.						
	Claim(s) <u>18 and 20</u> is/are objected to.						
8)[_]	Claim(s) are subject to restriction	n and/or election requirement.					
Applicat	ion Papers						
9)[	The specification is objected to by the E	xaminer.					
10)⊠	)⊠ The drawing(s) filed on <u>30 July 2003</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.						
	Applicant may not request that any objection	n to the drawing(s) be held in abe	yance. See 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the	·	• • •	` '			
11)	The oath or declaration is objected to by	the Examiner. Note the attach	ned Office Action or form P1	ГО-152.			
Priority (	ınder 35 U.S.C. § 119						
12)🖂	Acknowledgment is made of a claim for	foreign priority under 35 U.S.C	c. § 119(a)-(d) or (f).				
a)	a)⊠ All b)□ Some * c)□ None of:						
	1. Certified copies of the priority doc	cuments have been received.					
	2. Certified copies of the priority doc						
	3. Copies of the certified copies of the		en received in this National	Stage			
* 0	application from the International	, , , , , , , , , , , , , , , , , , , ,	-4 ' - I				
	See the attached detailed Office action fo	or a list of the certified copies n	ot received.				
Attachmen	t(s)						
1) 🛛 Notic	e of References Cited (PTO-892)	4) Interview	w Summary (PTO-413)				
2) 🔲 Notic	e of Draftsperson's Patent Drawing Review (PTO-	948) Paper N	lo(s)/Mail Ďate´. of Informal Patent Application (PTC	D 152)			
	nation Disclosure Statement(s) (PTO-1449 or PTC r No(s)/Mail Date		East search history.	J-132j			

### **DETAILED ACTION**

# **Priority**

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 17, 19, and 22 are rejected under 35 U.S.C. 102(b) as being anticipated by Naji (U.S. Patent No 6,304,477).

With respect to claim 17, Figure 1 of Naji discloses a nonvolatile memory device comprising a flip-flop unit (22, 26, 28, 29) including a NMOS latch (22, 26) for latching data, an access controller (34 and 35) for controlling a connection of a bit line (BL, BLN) to the flip-flop unit (22, 26, 28, 29) on an enable state of a wordline (WL), and a nonvolatile resistive memory device (12, 14) connected between the flip-flop unit (22, 26, 28, 29) and the access controller (34 and 35).

With respect to claim 19, the resistive memory device 12 would be a first resistive memory device and the element 14 would be a second resistive memory device, and such resistive memory device store different values (low resistance state or high resistance state) depending on the voltage applied to the bit lines.

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With respect to claim 22, transistors 21 and 25 in figure 1 of Naji would be considered as a current supply means, because when these transistors are ON, a current would be supplied to the resistive memory device (12 and 14).

# Allowable Subject Matter

Claims 1-16, 21, 23 are allowed.

Claims 18 and 20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The prior art of record fail to teach or suggest a memory device comprising a write/read controller, a nonvolatile resistive memory device coupled to receive write/read control signals from the write/read controller, and a logic switch having two or more switching states configured to select among the switching states depending on a logic value in the nonvolatile resistive memory device when the write/read signals are disabled (claims 1, 21 and 23): a connection of a nonvolatile resistive memory device between PMOS latch and NMOS (claim 7); a nonvolatile memory device comprising a flip-flop comprising NMOS and a nonvolatile resistive memory device connected between a power voltage terminal and the flip-flop (claim 12); a particular connection of the nonvolatile resistive memory device and the latch as recited in claims 18 and 20.

#### Conclusion

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The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

-Hurst, Jr. te al disclose a memory device having resistive memory device.

-Black et al disclose a memory device including a resistive memory device

-Fulkerson et al disclose a memory device having a resistive means.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Son Dinh whose telephone number is 571-272-1868. The examiner can normally be reached on 8am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on 571-272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-1868.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

S. Dinh August 20, 2004

> Son T. Dinh Primary Examiner

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